



IN THE UNITED STATES  
PATENT AND TRADEMARK OFFICE

Patent Application

Inventor(s) Vivian Wanda Ryan

Case 16-2

Serial No. 09/604,519

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Examiner Not Yet Assigned

Title Integrated Circuit And A Method Of Manufacturing An Integrated Circuit

THE COMMISSIONER OF PATENTS AND TRADEMARKS  
WASHINGTON, D. C. 20231

Attention: Office Draftsman

SIR:

Please make of record the attached three (3) sheet of formal drawings in the above-identified application.

Respectfully,

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Agere Systems Inc.

Date: 3/27/01

Attachment

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D. C. 20231 on 3/22/01 (Date of Deposit)

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